



Form PTO-1449 (modified)
List of Patents and Publications
For Applicant's Information
Disclosure Statement
(Use several sheets if necessary)

PATENT NO. 5500-78200

SERIAL NO. 09/927,058

APPLICANT: McGrath

GROUP: 2185

FILING DATE: August 9, 2001

U.S. PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	RECEIVED CLASS	FILING DATE IF APPROPRIATE
						DEC 05 2001	
						Technology Center 2100	

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

A1	Intel, "Intel Architecture Software Developer's Manual, Vol. 2: Instruction Set Reference," 1999, pages 3-681-3-687.
A2	AMD, "SYSCALL and SYSRET Instruction Specification", Application Note, 1998, pages 1-10.
A3	<u>The Technology Behind Crusoe™ Processors, Low-Power x86-Compatible Processors Implemented with Code Morphing™ Software</u> , Transmeta Corporation, January 2000, pages 1-18.
A4	<u>Alpha Learns to Do Windows; Digital's FX!32 is The Key To Running Win32 Software on Alpha/Windows NT.</u> , Selinda Chiquoine, BYTE, August 4, 1998, 4 pages.
A5	<u>Awards Stack Up for DIGITAL FX!32 Windows Compatibility Software for ALPHA</u> , DIGITAL Press Releases, December 11, 1997, 7 pages.
A6	<u>DIGITAL FX!32; White Paper: How DIGITAL FX!32 Works</u> , DIGITAL Semiconductor, January 26, 1998, 4 pages.
A7	<u>An Alpha in PC Clothing; Digital Equipment's New x86 Emulator Technology Makes An Alpha System a Fast x86 Clone</u> , Tom Thompson, BYTE, August 4, 1998, 7 pages.
A8	<u>AMD 64-Bit Technology; The AMD x86 Architecture Programmers Overview</u> , AMD, Publication #24108 Rev: A, August 2000, pages 1-106.
A9	<u>Addendum-Intel Architecture Software Developer's Manual</u> , Vol. 2: Instruction Set Reference, 1996/1997, Chapter 3, pages 3-1 to 3-15,
A10	<u>AMD 64-Bit Technology; The AMD x86-64 Architecture Programmers Overview</u> , AMD, Publication #24108 Rev: C, January 2001, pages 1-128.
A11	<u>Intel Architecture Software Developer's Manual</u> , Volume 1: Basic Architecture, 1997, pages 3-10 to 3-13.

EXAMINER:

DATE CONSIDERED:

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.